

<b>Notice of References Cited</b>	Application/Control No. 10/053,827		Applicant(s)/Patent Under Reexamination CHANG ET AL.	
	Examiner Thong H. Vu		Art Unit 2142	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,049,873 A	09-1991	Robins et al.	340/825.01
	B	US-5,982,780 A	11-1999	Bohm et al.	370/450
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Placement and Routing Tools for the Triptych FPGA - Carl Ebeling (1995) www.ece.nwu.edu/~hauck/publications/TripTools.ps
	V	Performance-Driven Interconnect Design Based on Distributed.. - Cong, Leung, Zhou (1993) cadlab.cs.ucla.edu/~ksleung/paper/csd920043.ps
	W	Traffic Shaping for Congestion Control in High Speed ATM Networks - Liu (1992) ftp.cs.usask.ca/pub/discus/thesis-liu.ps.Z
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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